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R Singh, CDS Inc, CA San Jose - Proc. CICC, 1999 - [ieeexplore.ieee.org](http://ieeexplore.ieee.org)

Page 1. A Review of Substrate Coupling Issues and Modeling Strategies **Raminderpal**

**Singh** Cadence Design Systems San Jose, California Abstract ...

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**A Benchmark Suite for Substrate Analysis**

E Charbon, LM Silveira, P Miliuzzi, IE de Sistemas - Proceedings of the 2000 conference on Asia South Pacific ... , 2000 - [ieeexplore.ieee.org](http://ieeexplore.ieee.org)

... layer. 619 Page 4. Fig. 2. Contact partitioning Loin Win Fig. 3. Layout of downconversion stage (Courtesy of **Raminderpal Singh**) There ...

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